Lewis Setter

7891 Anchor Dr | Longmont, 80504 CO | (913) 957-5271 | ljsetter@mines.edu

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| Objective |  |
| An internship with a focus on signal processing |  |
| Education |  |
| Colorado School of Mines | May 2019 |
| * B.S. Electrical Engineering * Computer Science Minor * 3.9 GPA * Related Coursework: embedded systems, analog systems, feedback control, data structures, thermodynamics * Upcoming Coursework: DSP, electromagnetics, software engineering in Java, algorithms |  |
| Skills & Abilities |  |
| Hardware |  |
| * Capable of developing microcontrollers and FPGAs for simple applications * Familiar with oscilloscopes, DMMs, power supplies * Comfortable soldering on PCBs |  |
| Software |  |
| * Fluent in C++, working with C in embedded systems * Learning MATLAB in the context of signal processing, simulation, data collection * Familiar with Verilog * Able to quickly model problems in Python |  |
| Communication |  |
| * Presented weekly research updates to a team of colleagues and professors * Collaborated with professors on instructional videos to expose new students to digital design |  |
| Leadership |  |
| * Led a small research group by organizing meetings and by assigning tasks to each member * Coordinated offensive and defensive schemes for Rugby in practice and during live play |  |
| Experience |  |
| Researcher, Energy Modeling Group  CSM Mechanical Engineering Dept | May 2017 to present |
| * Simulated energy use in single family homes to evaluate construction methods * Authored a journal paper regarding energy use in cross laminated timber buildings * Assisted with instrumentation in a thermal science lab |  |